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On the Use of Fujitsu A64FX Processor for Genome Sequence Analysis Workloads

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Genome sequence analysis

The declining cost and rising throughput of genome sequencing technologies, combined with the availability of complete reference genomes, are driving breakthroughs in:

- Personalized medicine
- Evolutionary biology
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However, while sequencing produces longer and more accurate reads, the computational cost of processing these sequences is growing significantly.

Opportunities for ARM HPC systems

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A notable example is Fujitsu's A64FX processor, the first Arm-based CPU to power a top-ranked supercomputer. Featuring:

- High memory bandwidth
- Scalable Vector Extension (SVE)

These capabilities make A64FX a powerful platform for genomics workloads with high data-level parallelism.

Opportunities for ARM HPC systems

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We compared A64FX's performance and efficiency to a baseline server class x86 system.

Evaluation methodology

CPU platforms:

1. Fujitsu A64FX processor with 48 cores and 32GB HBM2 (referred as a64fx).
2. A server class Intel Xeon Gold 5318N with 24 (48) cores (threads) processor (Cascade Lake architecture) with 192GB DDR4-2667 (referred as Xeon).

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Benchmarks:

Modern aligners - Wavefront Align [1] and BiWavefront Align [2]

Edit distance approximation - SneakySnake [3]

End-to-end framework - Minimap2 [4]

- [1] Marco-sola et al., Fast gap-affine pairwise alignment using the wavefront algorithm, Bioinformatics, 2020.
- [2] Marco-sola et al. Optimal gap-affine alignment in $O(s)$ space. Bioinformatics, 2023, vol. 39, no 2, p. btad074.
- [3] Alser et al., SneakySnake: a fast and accurate universal genome pre-alignment filter for CPUs, GPUs and FPGAs. Bioinformatics, 2020, vol. 36, no 22-23, p. 5282-5290.
- [4] Li, Heng. Minimap2: pairwise alignment for nucleotide sequences. Bioinformatics, 2018, vol. 34, no 18, p. 3094-3100.

Evaluation methodology

Input datasets:

Dataset	Read Length	No. of Pairs	Dataset Size
100bp_1	100	30M	6GB
250bp_1	250	30M	14GB
10Kbp	10,000	100K	2GB
30Kbp	30,000	100K	6GB

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For the end-to-end framework evaluation we used the reference and long input sequences provided by the minimap cookbook repository.

We evaluate the end-to-end execution of minimap2 going from index creation, through seeding and sequence alignment.

Evaluation methodology

Experiments:

Scalability

In each experiment, we utilize up to 24 threads, pinning each thread to a dedicated core.

To evaluate scalability, we begin with a single thread and increment the thread count in steps of four, up to the maximum of 24 threads.

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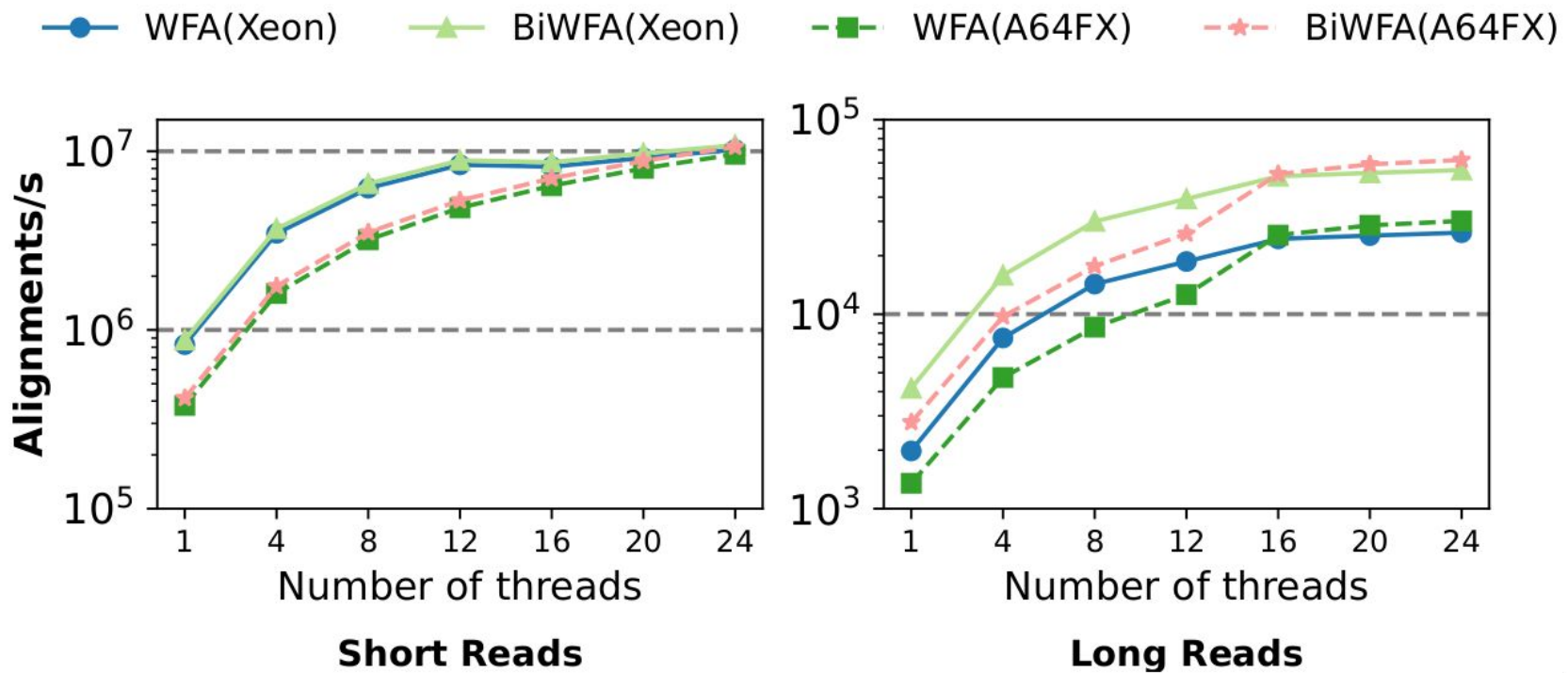
Vectorization

All evaluated benchmarks include optimized implementations that leverage the SIMD capabilities of both x86 (AVX512) and Arm (NEON) CPUs.

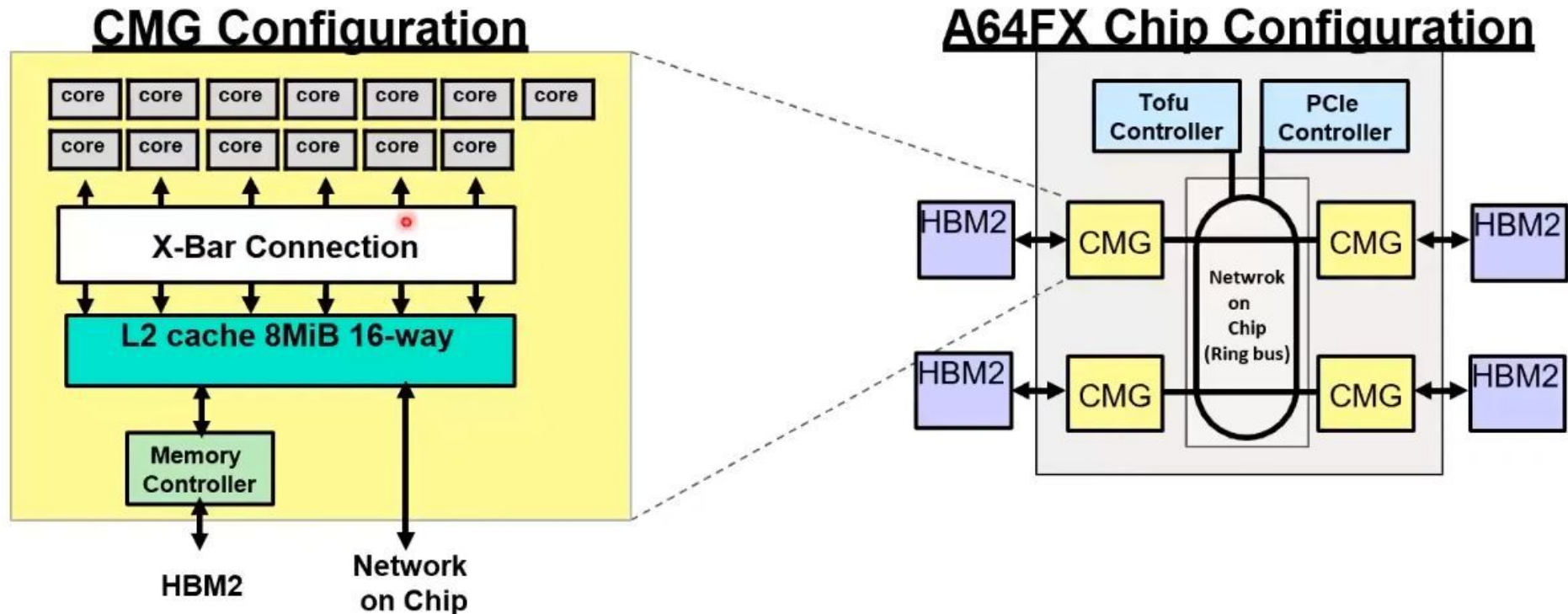
However, the A64FX processor supports the SVE ISA. Thus, we evaluate the performance impact of incorporating SVE optimizations across all benchmarks.

Scalability results

Modern aligners

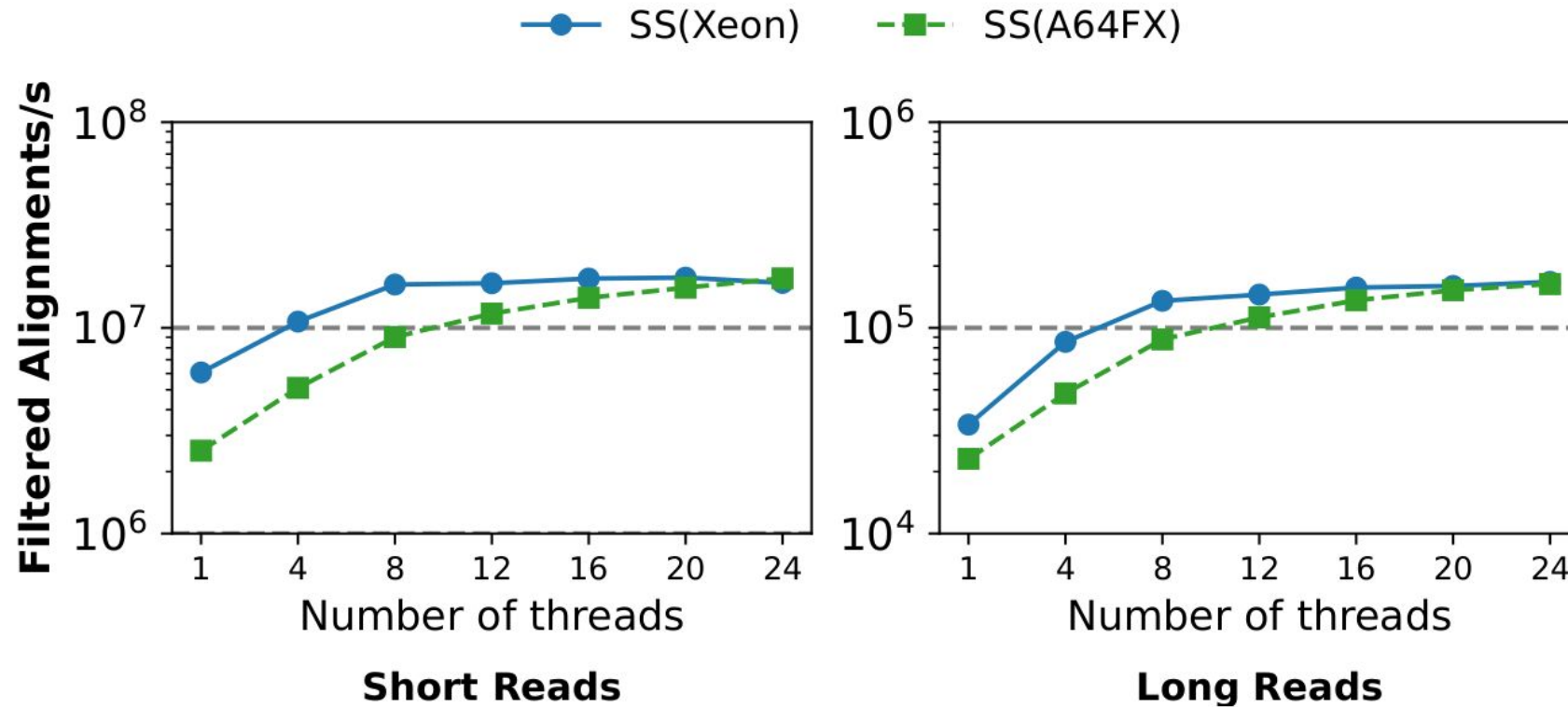


Scalability results



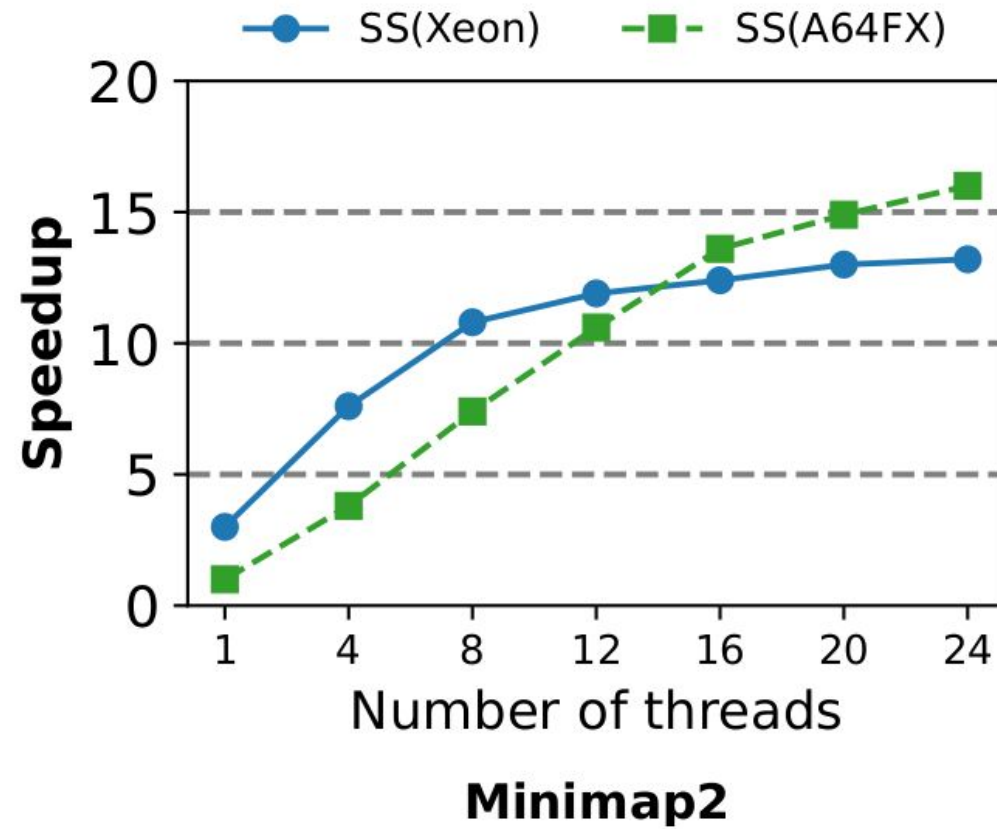
Scalability results

Edit distance



Scalability results

End-to-end framework



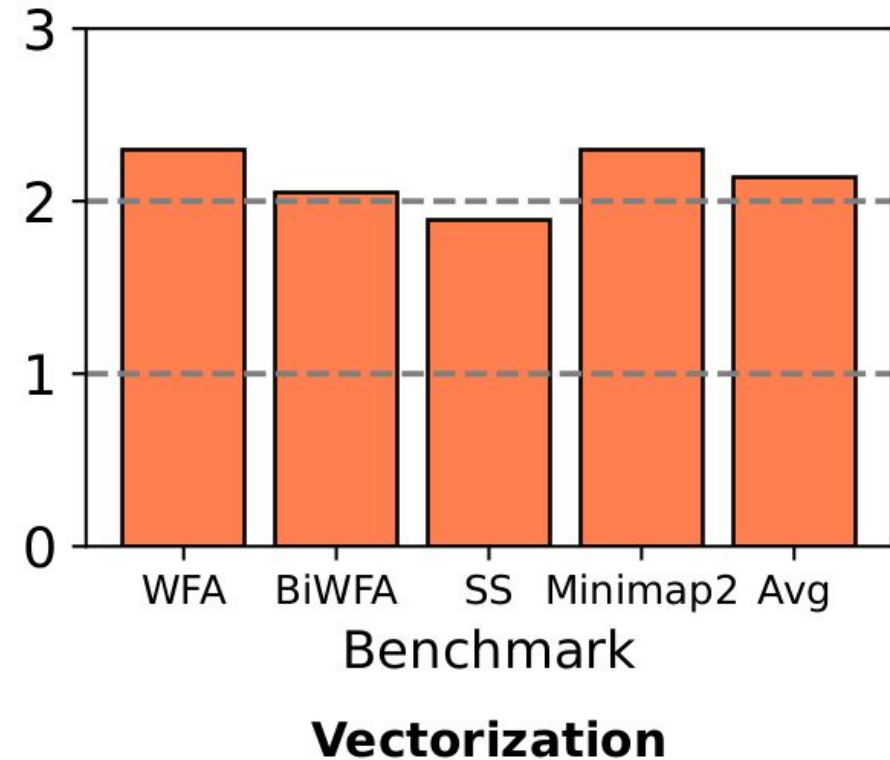
Impact of vectorization

In this experiment we compared ARM NEON vs ARM SVE.

All experiments used 24 threads.

On average, SVE outperforms by 2.1x to NEON.

This results come from the larger vector length and more sophisticated instructions, e.g., predication.



Conclusions and future work

A64FX outperforms Xeon in multi-threaded bioinformatics workloads, thanks to:

- High-bandwidth HBM2

- Efficient cache hierarchy

- Superior scalability

Xeon leads in single-threaded tasks, but suffers from memory bandwidth bottlenecks under parallel workloads.

SVE vectorization delivers an average 2.1× performance boost, highlighting the importance of ISA-aware optimizations for ARM platforms.

Conclusions and future work

Further bioinformatics pipeline tuning for SVE.

Exploration of Arm Neoverse platforms for scalable genomics.

Evaluation of NVIDIA Grace Hopper Architectures.



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